



PATENT
Conf. No.: 8432

IN THE UNITED STATES PATENT OFFICE

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RCE H18-06*

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: "A PROGRAMMABLE LOGIC DEVICE WITH
TIME-MULTIPLEXED INTERCONNECT"

Ser. No.:	10/810,034	File Date:	March 26, 2004
Examiner:	Vibol Tan	Art Unit:	2819
Docket No.:	X-1006-1D US	Conf. No.:	8432

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION

Dear Examiner:

In response to the Final Office Action mailed from the Patent Office on October 26, 2005, please amend the above-identified Claims as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.